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AMENDMENTS IN THE CLAIMS:

1. (Currently Amended) An LSI, comprising:

a RAM for storing an encrypted intermediate code;

a ROM for storing an interpreter execution program that is capable of interpreting the intermediate code; and

a CPU for controlling execution of the interpreter execution program, wherein the RAM, the ROM, and the CPU are formed on one chip.

- 2. (Canceled)
- 3. (Original) An LSI according to claim 1, wherein:

the RAM can store an encrypted intermediate code and an unencrypted intermediate code; and

the interpreter execution program can interpret both the encrypted intermediate code and the unencrypted intermediate code.

4. (Original) An LSI according to claim 1, further comprising:

a recording/reproduction head for recording/reproducing information on an optical disc; and

an optical disc control section for controlling a motor which drives the optical disc,

wherein the optical disc control section is formed on the one chip.

5. (Original) An optical disc apparatus, comprising:

an execution section for executing an interpreter execution program that is capable of interpreting an intermediate code, so as to generate a control command string; and

a control section for controlling recording/reproduction of information on an optical disc according to the control command string.

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6. (Currently Amended) An optical disc apparatus according to claim 5, wherein the execution section includes:

- a RAM for storing an encrypted intermediate code;
- a ROM for storing the interpreter execution program; and
- a CPU for controlling execution of the interpreter execution program.
- 7. (Original) An optical disc apparatus according to claim 6, wherein the RAM, the ROM, and the CPU are formed on one chip.
- 8. (Original) An optical disc apparatus according to claim 7, wherein the control section includes:

a recording/reproduction head for recording/reproducing information on the optical disc;

a motor for driving the optical disc; and

an optical disc control section for controlling the recording/reproduction head and the motor.

- 9. (Original) An optical disc apparatus according to claim 8, wherein the optical disc control section is formed on the one chip.
- 10. (Original) An optical disc apparatus according to claim 5, wherein the intermediate code is encrypted.
- 11. (Original) An optical disc apparatus according to claim 6, wherein:

the RAM can store an encrypted intermediate code and an unencrypted intermediate code; and

the interpreter execution program can interpret both the encrypted intermediate code and the unencrypted intermediate code.